

中華大學
企業徵才資料表

填表日期 民國 115 年 3 月 26 日

公司名稱	美商迪艾司科技有限公司	統一編號	93501177
公司地址	總部：新竹縣竹北市台元街 18 號 4 樓之 10 工廠：新竹縣湖口鄉文化路 19 號		
公司產業別	半導體設備供應商		
公司現有員工數	<input type="checkbox"/> 1-10 人 <input type="checkbox"/> 11-30 人 <input type="checkbox"/> 31-50 人 <input type="checkbox"/> 51-100 人 <input checked="" type="checkbox"/> 100 人以上		
聯絡人資料	姓名：Charlene Chang 電話：(03)560-1918 #205	信箱：Charlene.Chang@dis.tech 傳真機號碼：無	
職缺職稱	Hardware Design Engineer 硬體設計工程師	職缺部門(單位):邀約面談時說明	
需才系別	<input checked="" type="checkbox"/> 指定電機工程學系		
需才日期	即日起	應徵期限：	無
需才人數	1-2 人		
需才學歷	<input checked="" type="checkbox"/> 大學 <input checked="" type="checkbox"/> 碩士 <input type="checkbox"/> 其他_____		
工作時間	日班		
工作內容	<p>Role Summary/Purpose: Hardware design engineer will closely work with worldwide engineers to perform engineering works for hardware testing solution of next generation semiconductor devices. The work includes requirement analysis, feasibility study, solution evaluation, task planning, project management, design execution, quality control and verification. We are working on cutting edge requirement and future technology.</p> <p>Responsibilities:</p> <ul style="list-style-type: none"> • Provide global semiconductor interface test hardware solutions of next generation semiconductor devices for world-wide customers. • Provide chip test interface HW solution engineering to compare pros and cons of different approaches and recommend best option to customers considering both performance, lead time, cost. • Responsible for Testing circuits Design and super high layers PCB design for high complexity ATE device interface board correspond to various device testing, eg. Mobile application processor, High performance computer, AI, RF etc. • Responsible for scheme selection of a SUBSTRATE/MLO design in wafer testing, research for low Cost of Test scheme (considering TDE, Skip 		

	<p>DIE, substrate stack-up).</p> <ul style="list-style-type: none"> • Responsible for power integrity (PI) and signal integrity (SI) simulation at board level or system level, frequency domain or time domain to ensure HW product performance at design stage. • Implement complex mechanical design/simulation, cable design, thermal evaluation by collaborating with PCB design to achieve premium quality in hardware solution according to customer device testing ultimate challenges. • Responsible for global end to end hardware project management to ensure best quality and on time delivery: <ul style="list-style-type: none"> -Device testing requirement assessment and Feasibility study -Risk analysis and mitigation planning. -Schedule planning and project management. -Design execution. -Regular review with global internal and external customers. -Quality Control and Verification. • Work closely with Global supply chain, provide solution to solve manufacture (DFM), assembly (DFA) challenges, ensure hardware products on time delivery and very high first pass rate. • New technology research, new products, new materials evaluation for next generation device testing. • Deliver hardware design training and seminars to customers. <p>Requirement:</p> <ul style="list-style-type: none"> • Bachelor' s degree in Electronic Engineering, Micro-Electronics, Mechatronics, Communication Engineering, Automation, Instrumentation and Measurement, Biomedical engineering or similar major related to circuit design or hardware engineering. • Good knowledge and understanding of electronic circuit theory and application. • Knowledge and experience in multi-layer board design and embedded system design. • Knowledge in signal integrity or power integrity theory. • Good communication skills, good written and oral English. • Meticulous , pay attention to details.
<p>工作地點</p>	<p>(郵遞區號 302) <input type="checkbox"/>同公司地址 新竹縣竹北市台元街 18 號 4 樓之 10</p>
<p>工作性質</p>	<p><input type="checkbox"/>1. 兼職(長/短期) <input checked="" type="checkbox"/>2. 全職 <input type="checkbox"/>3. 其他：_____</p>
<p>薪資待遇</p>	<p><input checked="" type="checkbox"/> 其他：參照 104 職缺資訊</p>
<p>員工福利</p>	<p> 薪資與獎金制度 • 具市場競爭力的薪資制度，保障年薪 13 個月</p>

	<ul style="list-style-type: none"> • 獎金制度 • 優於勞基法之退休金提撥比例 <p> 休假制度 </p> <ul style="list-style-type: none"> • 優於勞基法之特別休假與彈性休假，入職即預給 12 天之特別休假 <p> 保險與健康照護 </p> <ul style="list-style-type: none"> • 團體保險保障：除依法享有勞保、健保外，員工及其配偶、子女皆享有免費團體保險保障 • 門診醫療補助：適用於員工本人及其配偶、子女 • 年度健康檢查：定期關心員工健康狀況 <p> 員工關懷與活動 </p> <ul style="list-style-type: none"> • 各式福委會補助與活動：包含旅遊補助、生育禮金、結婚禮金、家庭日、慶生會、電影欣賞等多元活動，促進員工交流與歸屬感 	
備註	歡迎您透過 E-mail 或 104 投遞履歷，誠摯邀請具備熱忱的您加入我們，一同成就更卓越的未來！	
刊登日期	115 年 3 月 26 日～ 115 年 12 月 31 日	未填寫起訖日期者一律刊登 15 日

本表填妥後轉成 pdf 檔案請 mail 至 chu_career@g.chu.edu.tw

其他相關事宜請洽

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